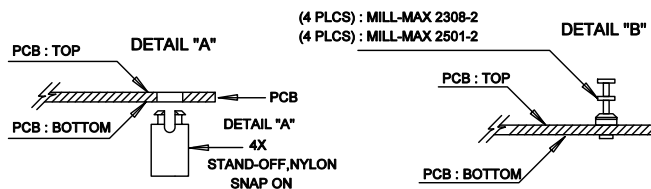
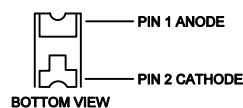
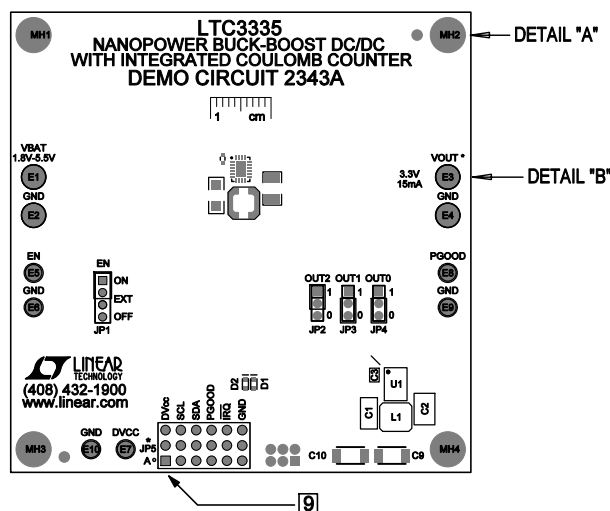


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	2	PRODUCTION FAB	BJS	06-30-15

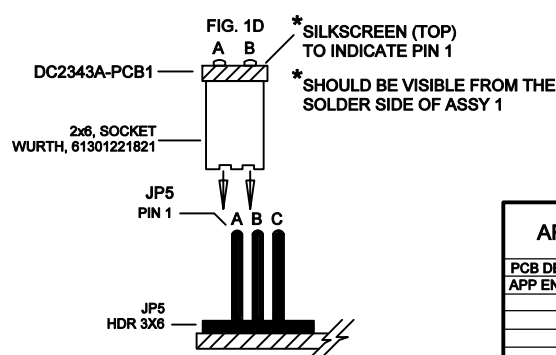
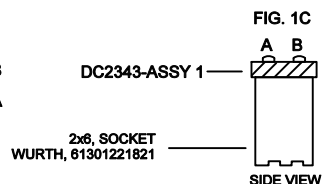
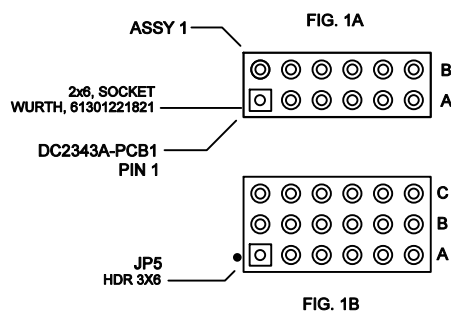
NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY REFLOW PROFILE SHALL BE IN ACCORDANCE WITH J-STD-020 WITH MAXIMUM SOLDER TEMPERATURE OF 250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL TURRETS, STAND-OFFS AND BANANA JACKS AS SHOWN BELOW:
8. INSTALL LED'S D1 AND D2 AS SHOWN:



9. INSTALLATION OF DC2343A-ASSY 1 FOR DC2343A, IS AS FOLLOWS:
REFER TO FIG. 1A, 1B, 1C AND 1D

- FOR ON BOARD PROCESSOR OPERATION PLUG ASSY 1 INTO JP5 ROWS C AND B.
- FOR DUST OR OTHER PROCESSOR CONNECTION PLUG DC2343A-1-ASSY1 INTO JP5 ROWS A AND B. CONNECT DVCC TO THE I/O SUPPLY OF THE EXTERNAL PROCESSOR. SIGNALS TO/FROM LTC3335 CONNECT TO JP5-AX.



APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.Linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
PCB DES.	NC		
APP ENG.	BJS	TITLE: TOP ASSEMBLY DRAWING: NANOPOWER BUCK-BOOST DC/DC WITH INTEGRATED COULOMB COUNTER	
		SIZE	IC NO. LTC3335EUD
		N/A	DEMO CIRCUIT 2343A
SCALE = NONE		REV.	2
		FILENAME: DC2343A-2.PCB	SHT 1 of 2